

A Low-Complexity Hyperspectral Anomaly Detection Algorithm and its FPGA Implementation

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Abstract—On-board real-time anomaly detection has always been a challenging task in hyperspectral imaging analysis as it requires low computational complexity. Most of the existing anomaly detection algorithms inevitably trade off intensive computational complexity for high detection accuracy. This paper presents a fast spectral-spatial anomaly detection algorithm with low complexity in hyperspectral images (HSIs) using morphological reconstruction and a simplified guided filter (Fast-MGD). Since the simple filtering techniques are applied, it is therefore feasible to achieve an FPGA-based (Field Programmable Gate Array) hardware implementation. More precisely, an effective deeply pipelined acceleration scheme is developed adopting high-level synthesis (HLS) to support HSIs that are acquired over different scenes with different sizes and spectral bands. Experimental results show strong advantages of the proposed FPGA-based Fast-MGD in processing speed and resource consumption while a high detection accuracy is remained. Its applicability in on-board real-time processing is demonstrated and verified.

Index Terms—Hyperspectral image (HSI), anomaly detection, Morphological reconstruction, guided filtering, high-level synthesis (HLS), real-time implementation.

I. INTRODUCTION

OWING to the richness and abundance in spectral-spatial information, hyperspectral images (HSIs) acquired by hyperspectral imaging, have been widely used in various applications including classification [1], target or anomaly detection [2], etc. Benefiting from the fact that anomaly detection that does not require any prior information nor rely on the complex

This work was supported in part by the National Natural Science Foundation of China under Grant 62071360, Grant 61801359, Grant 61571345, Grant 91538101, Grant 61501346, Grant 61502367 and Grant 61701360, in part by the Young Talent fund of University Association for Science and Technology in Shaanxi of China under Grant 20190103, in part by the Special Financial Grant from the China Postdoctoral Science Foundation under Grant 2019T120878, in part by the 111 project under Grant B08038, in part by the Fundamental Research Funds for the Central Universities under Grant JB180104, in part by the Natural Science Basic Research Plan in Shaanxi Province of China under 2019JQ153, Grant 2016JQ6023, and Grant 2016JQ6018, in part by the General Financial Grant from the China Postdoctoral Science Foundation under Grant 2017M620440, in part by the Yangtze Rive Scholar Bonus Schemes under Grant CJT160102, in part by the Ten Thousand Talent Program, and in part by the Science and Technology on Electro-Optic Control Laboratory and Aeronautical Science Foundation of China under Grant 6142504190206. (*Corresponding authors : Weiyong Xie*)

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preprocessing like atmospheric and radiometric correction, hyperspectral anomaly detection shows its high applicability for real-time processing on satellites. It is expected as an effective cutting-edge technology for military and civilian tasks, such as precision agriculture [3] and civilian search-rescue operations [4].

Conducting real-time anomaly detection from satellites has always been a challenging task. On-ground computing platforms of high performance involving multi-core processors and GPUs generally display their inabilities of being applied on satellites. In contrast, Field Programmable Gate Array (FPGA) is a better alternative to be adopted in the harsh environment of outer space for three reasons. Firstly, FPGA can provide much more competent levels of performance while sustaining lower power consumption compared with GPUs [5]. Secondly, FPGA offers high-level flexibility due to the inherent ability to change functionality through partial or full reconfiguration. Thirdly, the increasing characteristics of ionizing-radiation tolerance make FPGA the most extensive solution for on-board processing at earth observation satellites. Unfortunately, the hardware implementations on FPGA for on-board anomaly detection are few so far.

Aiming at solving problems of on-board real-time processing of hyperspectral anomaly detection, the motivations of our work is to develop an algorithm and hardware structure for both high accuracy and low complexity. In this paper, we propose a fast spectral-spatial anomaly detection algorithm based on morphological reconstruction and the simplified guided filtering (Fast-MGD). The proposed Fast-MGD is a segmentation-based method that can sequentially actuate operations of average fusion, feature location, feature extraction and feature clustering. The whole process can ensure high detection accuracy while avoiding complex matrix operations. Through studying the parallelism of Fast-MGD, FPGA-based hardware implementation is therefore proposed. In particular, a relatively mature high-level synthesis (HLS) [6] is especially utilized in obtaining better portability, higher flexibility, and shorter development period against the conventional RTL-based (register transfer level) design method, contributing a minor proportion to the above method's overall superiority.

The key contributions of this paper can be recapitulated as follows: 1) To satisfy low-complexity while maintaining detection performance, a simple but effective combination of the advantageous morphological reconstruction and self-guided filtering is designed. Especially, the self-guided filtering is further optimized by removing redundant mean operations on the intermediate coefficients to reduce memory requirements. 2) A novel and effective deep pipelined architecture is presented

to accelerate our algorithm on FPGA by adopting HLS, which can be easily reconstructed by modifying several parameters for different scenes of various sizes featured with different numbers of spectral bands, offering strong feasibility for on-board real-time processing. 3) An efficient parametric configurable sharing architecture is designed for both morphological reconstruction and self-guided filtering.

The remainder of this paper is organized as follows. Section II conducts an overall review of related work. Section III elaborates the principle of morphological filtering and guided filtering as well as the difficulties of implementing these two techniques. Section IV introduces the proposed Fast-MGD. Section V presents the FPGA implementation of Fast-MGD. Section VI provides experimental assessment on detection accuracy and processing performance of the proposed Fast-MGD and its hardware implementation on FPGA. Section VII summarizes conclusions.

II. REVIEW OF RELATED WORK

Various anomaly detection algorithms have been proposed in recent decades. The Reed-Xiaoli (RX) algorithm [7] proposed by Reed and Yu is regarded as a milestone in hyperspectral anomaly detection. Under the assumption that the background follows a single Gaussian normal distribution, RX detector detects anomalies by calculating the Mahalanobis distance between the pixel to be tested and the background obtained through estimating the sample mean and covariance matrix. Various variants of RX algorithms have been investigated to realize better detection performance [8], [9]. For instance, a concentric dual sliding window around each image pixel is adopted to estimate the background in the local RX detector (LRX) [10] proposed by Molero *et al.* However, the RX-based algorithm does not perform satisfactorily in complicated scenarios due to the linear assumption of the background.

Despite a kernel RX (KRX) proposed by Nasrabadi *et al.* [11] further exploited the nonlinear characteristics of HSIs, and mapped the spectral information of the original hyperspectral data to the high-dimensional feature space, yet, the performance of this method is not therefore improved noticeably, in which the required computation of high-order Gram matrix and its inverse matrix is far beyond affordability.

In addition, diverse methods of non-RX algorithms in terms of representation-based, projection-based, and segmentation-based have been proposed [12]–[16]. For instance, collaborative-representation-based detector (CRD) [13] employs the concept that each pixel in the background can be approximately represented by its spatial neighborhoods whereas anomalies cannot be. The collaboration of representation is reinforced by L2-norm minimization of the representation weight vector, in which a Euclidean distance-weighted regularization matrix is included to adjust the contribution of each neighboring pixel. However, the configuration of bi-window size in the algorithm is characterized with high sensitivity, posing detrimental impacts on its detection performance and processing speed.

To compensate the inadequate consideration of the spatial correlation in the above algorithms, a tensor-based method was

thus proposed in [15] to describe the spectral information and spatial information equivalently. In recent decades, morphological filtering has been widely used as a novel HSI spatial descriptor for spatial feature extraction [16]–[18]. Based on morphological attribute filtering and domain transform recursive filtering, Kang *et al.* [16] proposed a segmentation-based detector (AED) to integrate spatial information throughout feature extraction process. The area-based attribute filtering is developed to extract anomalous candidates with specific area properties, yet, the gradient reversal cannot be thoroughly avoided by the domain transform filtering conducted in the post-processing operation. To tackle with the aforementioned problems, Xie *et al.* [18] proposed a more efficient strategy based on structure tensor and guided filtering (STGF), but the preprocessing operation of structure tensor-based band selection is relatively complicated. It is worth emphasizing that the global tree-representation-based strategy for morphological attribute filtering in these methods is confronted with lots of obstacles in hardware implementation.

In recent years, theories of deep neural networks (DNNs) attack detection have drawn much attention worldwide [19]–[23]. In [20], the adaptive weight deep belief network (DBN) with autoencoder structure is used to acquire high-level features and reconstruction errors. Xie *et al.* [22] proposed an autoencoder and adversarial-learning based semisupervised background estimation model (SBEM), which leverages the training spectral vectors obtained by a specific searching method to learn the background spectral characteristics. In [23], the generative adversarial network (GAN) is applied and developed to estimate the background by means of two effective constraints (a continuity representation constraint in the latent space and a discriminator based authenticity constraint). However, these methods need to recover and store the reconstructed image by the same size as the original HSI, thereby imposing enormous pressure on storage. A spectral-spatial feature extraction method (SSFE) [21] was proposed by Lei *et al.* through performing a linear combination of joint spectral and spatial detection results. However, the designed DBN extracting reduced number out of spectral features cannot perform satisfactorily in all HSIs due to the shortage in training samples.

Obviously, most of the aforementioned anomaly detectors improve their detection accuracy by raising the intensity of computation complexity, in contrast, lowering the computational cost is indeed taken into account by certain researchers, which has so far met with limited success. A huge amount of complicated computational operations regarding covariance matrix, inverse matrix and eigenvalue decomposition are still unavoidable, posing negative encumbrance for real-time on-board detection.

Furthermore, on-board anomaly detection has stringent requirements on size, weight, power consumption and radiation-hardened of the computing platform, to which FPGA stands out as a mainstream criterion for on-board hyperspectral data processing among the existing high performance computation devices. For instance, Yang *et al.* [5] used a streaming background statistics (SBS) approach for optimizing the constrained energy minimization (CEM) and RX on FPGA. In

[24], a lightweight network based on the quantization and the structured pruning (P-Q-AD) was proposed by utilizing the potential relationship between detection accuracy and throughput. However, most of the cutting-edge algorithms are not sufficiently implemented on FPGAs due to their intrinsic computational complexity and low parallelism.

To summarize, developing hyperspectral satellite remote sensing detection via algorithm and hardware implementation becomes an urgent must that focus on solving the key problem about opportune acquisition of abnormal targets.

III. BACKGROUND

A. Morphological filtering

Morphological filtering is defined between a group of image points called object and kernel structuring element (SE) [25], which can be used as a powerful tool for extracting useful image components representing region shapes. Morphological operations are developed and further extended based on two basic operations, erosion and dilation.

As one of the ingenious morphological operations, morphological reconstruction requires two input images which consist of a marker image functioning as the starting point for transformation and a mask image constraining the transformation.

In this work, we consider rectangular flat SE \mathbf{b} of size $r \times r$. Let two grayscale images \mathbf{f} and \mathbf{g} denote the marker image and the mask image respectively, to which \mathbf{f} is pointwise less than or equal to \mathbf{g} . The morphological dilation reconstruction (R^D) of \mathbf{g} from \mathbf{f} is denoted by

$$R_{\mathbf{g}}^D(\mathbf{f}) = D_{\mathbf{g}}^{(k)}(\mathbf{f}) \quad (1)$$

where $D_{\mathbf{g}}^{(1)}(\mathbf{f}) = (\mathbf{f} \oplus \mathbf{b}) \wedge \mathbf{g}$, $D_{\mathbf{g}}^{(n)}(\mathbf{f}) = D_{\mathbf{g}}^{(1)}(D_{\mathbf{g}}^{(n-1)}(\mathbf{f}))$ for $2 \leq n \leq k$, $k, n \in \mathbb{N}^+$ satisfies $D_{\mathbf{g}}^{(k)}(\mathbf{f}) = D_{\mathbf{g}}^{(k-1)}(\mathbf{f})$. The symbol \oplus stands for the morphological dilation, and \wedge denotes the pointwise minimum at each pixel of two images.

Similarly, if \mathbf{f} is pointwise greater than or equal to \mathbf{g} , the morphological erosion reconstruction (R^E) of \mathbf{g} from \mathbf{f} is expressed as

$$R_{\mathbf{g}}^E(\mathbf{f}) = E_{\mathbf{g}}^{(k)}(\mathbf{f}) \quad (2)$$

where $E_{\mathbf{g}}^{(1)}(\mathbf{f}) = (\mathbf{f} \ominus \mathbf{b}) \vee \mathbf{g}$, $E_{\mathbf{g}}^{(n)}(\mathbf{f}) = E_{\mathbf{g}}^{(1)}(E_{\mathbf{g}}^{(n-1)}(\mathbf{f}))$ for $2 \leq n \leq k$, $k, n \in \mathbb{N}^+$ satisfies $E_{\mathbf{g}}^{(k)}(\mathbf{f}) = E_{\mathbf{g}}^{(k-1)}(\mathbf{f})$. The symbol \ominus stands for the morphological erosion, and \vee denotes the pointwise maximum at each pixel of two images.

The aforementioned operations can be combined into two more complex operations used in our work following certain sequences: opening and closing reconstructions, which show their better performance in extracting spatial features. The opening reconstruction can be conducted through a series of successive erosions followed by a dilation reconstruction. By duality, the result of the dilation of the input image followed by an erosion reconstruction is called closing reconstruction.

The erosion/dilation performed firstly in the opening/closing reconstruction is a window-based operation, which aims to find minimum/maximum among the pixels belonging to the SE that is characterized by its size and shape. A common method requires $l - 1$ comparisons if the SE is a local window containing l pixels, which results in redundancy of comparison,

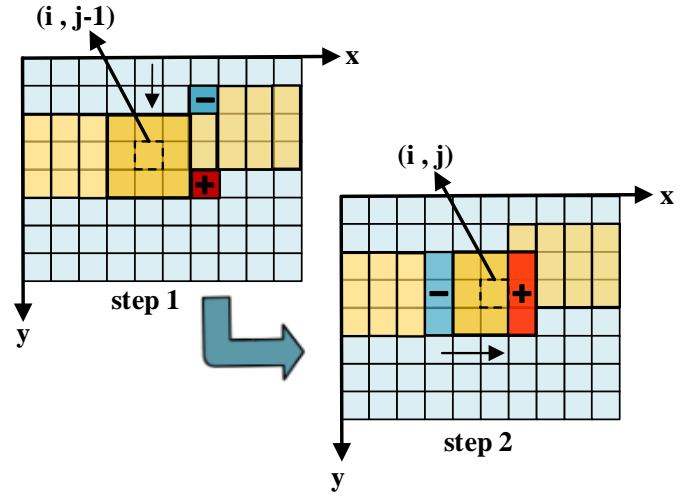


Fig. 1. The mean filtering processing in [26].

thereby exposing its extremely low computational efficiency. Van Herk [27] and Werman [28] *et al.* proposed a recursive algorithm (HGW) whose complexity is size-independent of the SE. However, we emphasize that the optimal comparison number does not always guarantee the best overall performance, to which latency and memory also matters significantly. The parallel architecture [29] proposed by Mukherjee *et al.* for rectangular elements reduces the processing time by simultaneously processing pixels within adjacent windows of SEs. The xfOpenCV library of Xilinx [30] provides dilation/erosion kernel being capable of processing SEs with various shapes, achieving good performance in terms of memory utilization and processing time. In our work, the data reuse structure based on SE decomposition is further designed, which not only reduces the number of comparisons, but also exhibits excellent performance in terms of latency and memory through keeping and reusing partial results generated during the computation process.

The subsequent dilation/erosion reconstruction in the opening/closing reconstruction can be essentially explained as constrained dilation and erosion operations. Vincent *et al.* [31] proposed four effective strategies in 1993 involving standard technique, sequential reconstruction (SR), reconstruction using a queue of pixels, and hybrid reconstruction. Furthermore, a single pass reconstruction algorithm was proposed by Robinson *et al.* [32], i.e., downhill filtering strategy. Anaconda-Mosquera *et al.* [33], [34] proposed effective hardware architectures based on SR and hybrid reconstruction, respectively. In [35], the morphological coprocessor unit (MCPU) is proposed for reconstruction operations based on standard technique by using large SE pipelines and interconnective architecture of the pipelines. We choose the simplest standard technique that can be directly obtained according to the definitions of Equation (1) and (2). However, this method is inapplicable for hardware implementation due to uncertainty of the iterations under idempotence [17]. Specifically, for the certain missions in hyperspectral anomaly detection, we adopt fixed iterations through conducting a large number of experiments to avoid the above problem.

B. Guided Filtering

The guided filter [36] computes the filtering output by considering the content of a guidance image. Here, we will briefly describe the rationale of guided filters.

Assuming that the output image \mathbf{q} is a linear transformation of the guidance image \mathbf{O} in a local window ω_j centered at the pixel j :

$$\mathbf{q}_i = a_j \mathbf{O}_i + b_j, \forall i \in \omega_j \quad (3)$$

where (a_j, b_j) are the linear coefficients and ω_j is a square window of a radius r . To determine the linear coefficients (a_j, b_j) , we need to seek a solution that minimizes the difference between the input image \mathbf{p} and the output image \mathbf{q} while maintaining the linear model (3). Specifically, we minimize the following cost function in the window ω_j :

$$E(a_j, b_j) = \sum_{i \in \omega_j} ((a_j \mathbf{O}_i + b_j - \mathbf{p}_i)^2 + \epsilon a_j^2) \quad (4)$$

where ϵ is a regularization parameter penalizing large a_j . Equation (4) is the linear ridge regression model and its solution is given by

$$a_j = \frac{\frac{1}{|\omega_j|} \sum_{i \in \omega_j} \mathbf{O}_i \mathbf{p}_i - \mu_j \bar{\mathbf{p}}_j}{\sigma_j^2 + \epsilon} \quad (5)$$

$$b_j = \bar{\mathbf{p}}_j - a_j \mu_j \quad (6)$$

where μ_j and σ_j^2 are the mean and variance of \mathbf{O} in ω_j , $|\omega_j|$ is the number of pixels in ω_j . $\bar{\mathbf{p}}_j = \frac{1}{|\omega_j|} \sum_{i \in \omega_j} \mathbf{p}_i$ is the mean of \mathbf{p} in ω_j .

After computing (a_j, b_j) for all window ω_j in the image, we compute the filtering output by

$$\mathbf{q}_i = \bar{a}_i \mathbf{O}_i + \bar{b}_i \quad (7)$$

where $\bar{a}_i = \frac{1}{|\omega|} \sum_{j \in \omega_i} a_j$ and $\bar{b}_i = \frac{1}{|\omega|} \sum_{j \in \omega_i} b_j$ are the average coefficients of all windows overlapping i . We compute the filter output \mathbf{q} from its definition (5), (6) and (7) according to Algorithm 1.

In addition to the edge-preserving property without gradient reversal artifacts, guided filtering naturally has a fast and non-approximate linear time algorithm that can be decomposed into a series of mean filters (f_{mean} in Algorithm 1) with windows radius r .

It is obvious to note that mean filter is also a window-based operation that is similar to the aforementioned erosion/dilation in the previous subsection. At present, one of the widely used techniques utilizes a custom mean filter derived from the approach in [26], [37], [38], of whose main idea lies in maintaining a sum for each column in the image to be filtered. The detailed process of mean filtering is shown in Fig 1. In specific, through subtracting its topmost old pixel and adding the bottom new pixel, the column sum is updated firstly. Then, through subtracting its leftmost column sum and adding the updated column sum, the window therefore moves to the right and its sum is updated. In our work, we further optimize the above architecture by studying the commonness between erosion/dilation and mean filter, and design a general frame which can be available for erosion/dilation and mean filter simultaneously.

Algorithm 1 Pseudocode of the Guided Filter

Input: \mathbf{p} : input image \mathbf{O} : the guidance image

Parameters: r, ϵ

$$mean_{\mathbf{p}} = f_{mean}(\mathbf{p}) \quad (1)$$

$$mean_{\mathbf{O}} = f_{mean}(\mathbf{O}) \quad (2)$$

$$Corr_{\mathbf{O}} = f_{mean}(\mathbf{O} * \mathbf{O}) \quad (3)$$

$$Corr_{\mathbf{pO}} = f_{mean}(\mathbf{p} * \mathbf{O}) \quad (4)$$

$$Var_{\mathbf{O}} = Corr_{\mathbf{O}} - mean_{\mathbf{O}} * mean_{\mathbf{O}} \quad (5)$$

$$Cov_{\mathbf{pO}} = Corr_{\mathbf{pO}} - mean_{\mathbf{p}} * mean_{\mathbf{O}} \quad (6)$$

$$a = Cov_{\mathbf{pO}} / (Var_{\mathbf{O}} + \epsilon) \quad (7)$$

$$b = mean_{\mathbf{p}} - a * mean_{\mathbf{O}} \quad (8)$$

$$mean_a = f_{mean}(a) \quad (9)$$

$$mean_b = f_{mean}(b) \quad (10)$$

$$\mathbf{q} = mean_a * \mathbf{O} + mean_b \quad (11)$$

Output: \mathbf{q}

Algorithm 2 Pseudocode of Self-Guided Filter

Input: \mathbf{I}

Parameters: r_b, ϵ

$$meanI = f_{mean}(\mathbf{I}) \quad (1)$$

$$CorrI = f_{mean}(\mathbf{I} * \mathbf{I}) \quad (2)$$

$$VarI = CorrI - meanI * meanI \quad (3)$$

$$a = VarI / (VarI + \epsilon) \quad (4)$$

$$b = meanI - a * meanI \quad (5)$$

$$\mathbf{V} = a * \mathbf{I} + b \quad (6)$$

Output: \mathbf{V}

According to Algorithm 1, it can be observed that there is strong data dependence in guided filtering, which limits the improvement of processing speed. Consequently, we design a high-throughput hardware architecture that eliminates data dependency, imposes reasonable simplification and segmentation for matrix operations.

IV. PROPOSED APPROACH

In this section, the proposed algorithm is described in detail. As shown in Fig. 2, the proposed Fast-MGD approach consists of four steps. Firstly, average fusion is adopted to reduce dimension of the HSIs. Secondly, morphological opening and closing reconstruction are utilized for feature location. Thirdly, a differential operation is conducted to extract anomalies in the HSIs. Finally, the final detection map is therefore obtained by means of the simplified self-guided filtering for feature clustering.

A. Average Fusion

The high dimensionality of the original HSIs will undoubtedly and inevitably incur extremely high computational complexity, throughout which massive storage capability is therefore required. The map of correlation coefficients of spectral bands in Fig. 3(a) shows that the adjacent bands are usually highly correlated with each other and contain redundant information. Besides, the correlation coefficient between adjacent bands can be calculated by the following equation:

$$\rho_{i,j} = \frac{Cov(\mathbf{g}_i, \mathbf{g}_j)}{\sqrt{Var(\mathbf{g}_i) Var(\mathbf{g}_j)}} \quad (8)$$

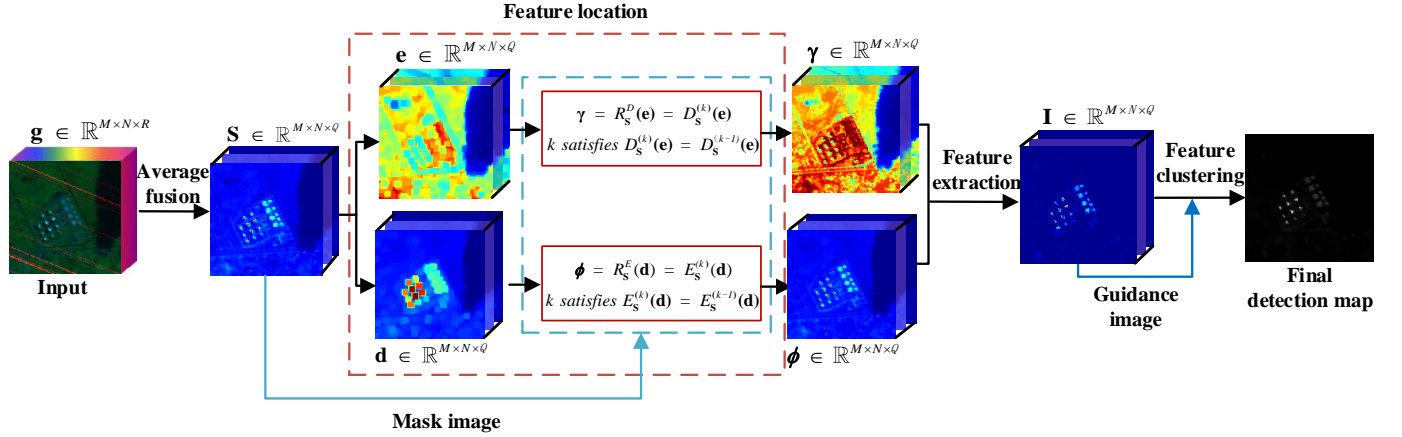


Fig. 2. Schematic of the proposed Fast-MGD anomaly detection in HSI.

where cov and var are the covariance and variance. \mathbf{g}_i and \mathbf{g}_j refer to the i -th and j -th hyperspectral bands. $i=1, 2, \dots, R-1$. As shown in Fig. 3(b), the highest correlation coefficient is 0.99996, and the lowest is 0.89014. Therefore, it is preferable to exploit specific methods for feature reduction.

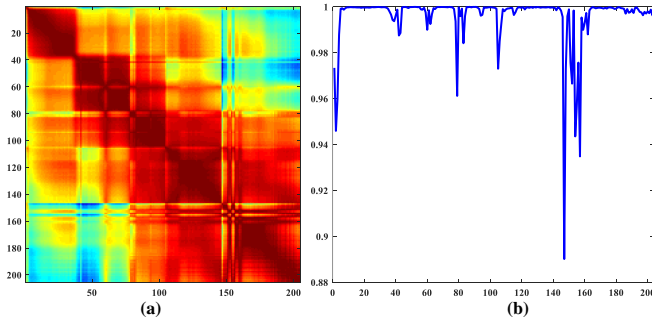


Fig. 3. (a) The maps of correlation coefficients of spectral bands. (b) The curve of correlation coefficients of adjacent spectral bands.

Based on this observation, the original HSI is therefore first partitioned into Q subsets of adjacent bands as follows:

$$\mathbf{C}^q = \begin{cases} (\mathbf{g}_{\lceil R/Q \rceil(q-1)+1}, \dots, \mathbf{g}_{\lceil R/Q \rceil q}) & 1 \leq q \leq Q-1 \\ (\mathbf{g}_{\lceil R/Q \rceil(q-1)+1}, \dots, \mathbf{g}_R) & q = Q \end{cases} \quad (9)$$

In the above expression, \mathbf{C}^q ($q = 1, 2, \dots, Q$) indicates the q th hyperspectral subset. $\mathbf{g} = (\mathbf{g}_1, \mathbf{g}_2, \mathbf{g}_3, \dots, \mathbf{g}_R)$ denotes an HSI with R spectral bands and $M \times N$ pixels in the spatial domain. $\lceil R/Q \rceil$ represents the smallest integer greater than or equal to R/Q .

Then a simple and effective average fusion method [39] is adopted to reduce spectral dimension by combining the complementary information of adjacent bands in each subset. Specifically, the fused band \mathbf{S}^q is obtained by the following equation:

$$\mathbf{S}^q = \frac{\sum_{i=1}^{N_q} \mathbf{C}_i^q}{N_q} \quad (10)$$

where \mathbf{C}_i^q represents the i th band in the q th hyperspectral subset, and N_q is the total number of spectral bands in the

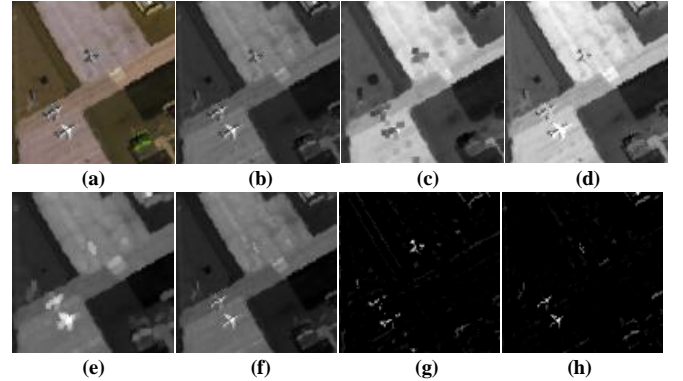


Fig. 4. Visualization of intermediate results on the San Diego dataset (using $r_a = 3$, $k = 20$). (a) Hyperspectral image. (b) The first fused image \mathbf{S}^1 . (c) and (d) are the intermediate results of the opening reconstruction. (e) and (f) are the intermediate results of the closing reconstruction. (g) and (h) are the result of $|\phi^1 - \mathbf{S}^1|$ and $|\mathbf{S}^1 - \gamma^1|$ respectively.

q th subset. As shown in Fig. 4(b), the fused image retains the anomalies while effectively removes noise and redundant information to some extent.

B. Feature Location

In this stage, the opening and closing reconstruction are applied for feature localization, in which a basic operand, a rectangular SE \mathbf{b} of size $r_a \times r_a$ is therefore defined. The erosion and dilation of \mathbf{S} by \mathbf{b} are first calculated as the marker images for dilation and erosion reconstruction, respectively. Let γ and ϕ denote the opening and closing reconstruction respectively. The operations performed at this stage can be expressed by the following equations:

$$\begin{aligned} \gamma^q &= R_{\mathbf{S}^q}^D(\mathbf{e}^q) = D_{\mathbf{S}^q}^{(k)}(\mathbf{S}^q \ominus \mathbf{b}) \\ &= D_{\mathbf{S}^q}^{(1)}(D_{\mathbf{S}^q}^{(k-1)}(\mathbf{S}^q \ominus \mathbf{b})) \\ \phi^q &= R_{\mathbf{S}^q}^E(\mathbf{d}^q) = E_{\mathbf{S}^q}^{(k)}(\mathbf{S}^q \oplus \mathbf{b}) \\ &= E_{\mathbf{S}^q}^{(1)}(E_{\mathbf{S}^q}^{(k-1)}(\mathbf{S}^q \oplus \mathbf{b})) \end{aligned} \quad (11)$$

where $q = 1, \dots, Q$ and SE \mathbf{b} is also used to perform erosion and dilation reconstruction. However, the morphological reconstruction following its definition (Equation (1) and (2))

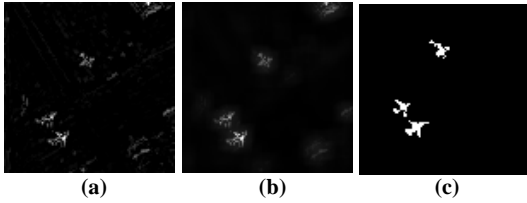


Fig. 5. (a) are the results of the differential operation ($\phi^1 - \gamma^1$). (b) are the results of the self-guided filtering (\mathbf{V}^1). (c) The reference map.

is an iterative procedure that is performed until reaching idempotence, which results in unpredictable time performance on hardware implementation. Fortunately, we experimentally proved that fixed iterations (k) can guarantee high accuracy in section VI. Compared with the area-based attribute filtering applied in STGF [18] and AED [16], the morphological opening and closing reconstruction are easier to implement.

As shown in Fig. 4(c)-(f), the intermediate results on the San Diego dataset are visual, and the number of iterations in both opening reconstruction and closing reconstruction is set to 20. Taking the opening reconstruction operation as an example, the erosion operation (Fig. 4(c)) is first performed to remove bright features smaller than the size of SE. However, the size of the dark feature is increased and the background is darkened. Subsequently, the reprocessing of the dilation reconstruction (Fig. 4(d)) removes the bright features while reducing its impacts imposed on dark features and background. In other words, opening reconstruction only removes bright features on a constant background. Conversely, the closing reconstruction only removes dark features within a constant background.

C. Feature Extraction

Considering that anomaly objects in HSIs usually appear as small-area objects compared with the background [16], the anomaly objects (Fig. 5(a)) are extracted through the differential operation, which merges the dark (Fig. 4(g)) and light (Fig. 4(h)) features from the above stage. For $q = 1, \dots, Q$, the process can be expressed as the following equation:

$$\mathbf{I}^q = |\phi^q - \mathbf{S}^q| + |\mathbf{S}^q - \gamma^q| = \phi^q - \gamma^q \quad (12)$$

D. Feature Clustering

Although morphological reconstruction can effectively extract small-sized bright and dark objects in the image, it is difficult for the reconstruction to filter out some background interference and random noise, to which may increase the false alarm rate of anomaly detection. However, as a local linear model that sufficiently utilizes the local spatial information of the image, guided filtering is a better alternative in solving the aforementioned problems.

The guided filter computes the filtering output by considering the content of a guidance image, which can be the input image itself or another different image. In this paper, we consider the special case in which the input image is used as the guide. The results of self-guided filtering can be defined as follows:

$$\mathbf{V}_i^q = a_j^q \mathbf{I}_i^q + b_j^q, \forall i \in \omega_j^q \quad (13)$$

The coefficients can be calculated as

$$a_j^q = \frac{\frac{1}{|\omega_j^q|} \sum_{i \in \omega_j^q} (\mathbf{I}_i^q)^2 - (\mu_j^q)^2}{(\sigma_j^q)^2 + \epsilon} \quad (14)$$

$$b_j^q = \mu_j^q - a_j^q \mu_j^q$$

where μ_j^q and σ_j^{q2} are the mean and variance of \mathbf{I}^q in the local window ω_j^q of size $(2r_b + 1) \times (2r_b + 1)$, ϵ is a parameter that controls the smoothness of the filtering, $q = 1, \dots, Q$. Inspired by [40], we eliminate two redundant mean filters (step (9) and (10) in the Algorithm 1). In order to prove the effectiveness of this method, we conducted a lot of experiments in MATLAB using a and b instead of their mean. Our experimental results in section VI showed that this modification imposes small impact on accuracy while greatly reducing the memory consumption. The self-guided filter applied in this paper is described in Algorithm 2. As shown in Fig. 5(b), it can be observed that the local spatial strong intercorrelation amidst adjacent pixels is utilized efficiently in feature clustering. Moreover, the background noise is removed in contrast to Fig. 5(a).

Finally, we perform the following operation to obtain the detection map:

$$\mathbf{O} = \sum_{q=1}^Q \omega_q \mathbf{V}^q \quad (15)$$

where $\omega_q = 1/Q$ ($q = 1, \dots, Q$) is the set, meaning that the processed result of each fused band contributes equally to finding anomaly objects.

V. FPGA IMPLEMENTATION

This section discusses the detailed implementation of our proposed algorithm. An overall hardware structure of Fast-MGD is given in subsection V-A. Subsection V-B describes the microscopic hardware architecture of Fast-MGD in detail. The last subsection explains our design optimization strategies in FPGA implementation.

A. Overall Hardware Architecture of Fast-MGD

As shown in Fig. 6, the framework of Fast-MGD is mainly composed of two components including an off-chip memory (DDR3 SDRAM) for caching the HSI data and a processor core, which can be directly used for on-board real-time processing. The processor core that encompasses three modules is used for the major purpose of data processing. The first module is the average fusion unit (AFU) which is in charge of reducing the spectral dimensions of a given HSI. The second module is the feature location and extraction unit (FLEU), of which is designed to extract anomalies based on morphological reconstruction. The last module is the feature clustering unit (FCU) being applied to further rectify the detection map through the self-guided filtering. Each module contains Q identical processing elements (PE) that work in parallel. It is emphasized that only deep pipelined design highlighted by GRAY in the figure is concerned while the rest of the figure only helps us to verify the function and performance of the proposed Fast-MGD hardware implementation.

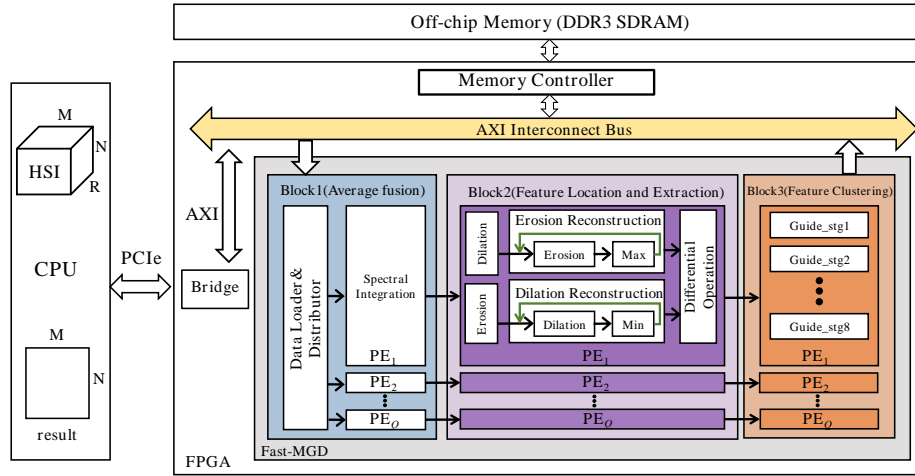


Fig. 6. Overall hardware structure of Fast-MGD.

B. Microscopic Hardware Architecture of Fast-MGD

1) *AFU*: The Data Loader and Distributor reads HSIs from the DDR3 SDRAM through the AXI interconnect bus, and loads pixel vectors by bands for AFU. The data width of DDR3 SDRAM is set to 512-bit, which is the maximum available bit-width of the device. Considering the case of an HSI with a pixel width by 16 bits, each address can store 32 successive bands of a pixel vector. The pixel derived from the Data Loader and Distributor is first stored in FIFO performing with 512 bit-width, and then the high-bits data is transferred to the Q Spectral Integration units, thereby reducing processing time through full use of the bandwidth of DDR3 SDRAM.

The Spectral Integration conducts sum and average operations along the spectral dimension during the data-width conversion (512 bits data is split into 16 bits data). The final result is stored in the FIFO and is delivered to the next module.

2) *FLEU*: As shown in Fig. 6, the opening and closing reconstruction (the erosion/dilatation followed by dilatation/erosion reconstruction) first work in parallel, and then the differential operation is executed for feature extraction. The two stages transmit the data stream through a memory buffer, whereby a task-level pipeline architecture is therefore designed.

As can be seen from the equation (11) in Section IV, the two basic image processing pipelines, referred to as erosion and dilatation, are the core of the morphological reconstruction. Specifically, the two pipelines can be configured with registers and memory buffers to design more complex dilatation/erosion reconstruction utilizing standard techniques.

The dilatation/erosion computes the maximum/minimum for each pixel within a customizable rectangular SE. As depicted in Fig. 8, the dilatation/erosion can be further subdivided into the row processing unit (RPU) and column processing unit (COPU).

The RPU reads the pixel in sequence from the FIFO as the dilatation is initiated. Within each cycle, the new pixel and the pixels involved in the current shift registers of size $r_a - 1$ are first compared in the row diversity unit (RDU), then the pixels in the registers are updated via reading the new pixel and removing the old one. We point out that the *array_partition*

directive in HLS is utilized to completely decompose the array into individual elements. The comparison result is passed to COPU through the shift-register.

```

using namespace hls;
#define iteration 20
#define fusion_band 2
void morph_close(stream<datatype_16nt> in[fusion_band], stream<datatype_16nt> ori[fusion_band],
                stream<datatype_16nt> out[fusion_band]){
#pragma HLS DATAFLOW
    stream<datatype_16nt> mid1[iteration][fusion_band];
#pragma HLS STREAM variable=mid1 depth=1 dim=2
    stream<datatype_16nt> ori_1[iteration][fusion_band];
#pragma HLS STREAM variable=ori_1 depth=1 dim=2
    stream<datatype_16nt> mid2[iteration][fusion_band];
#pragma HLS STREAM variable=mid2 depth=1 dim=2
    stream<datatype_16nt> ori_2[iteration][fusion_band];
#pragma HLS STREAM variable=ori_2 depth=1 dim=2
    stream<datatype_16nt> mid3[fusion_band];
#pragma HLS STREAM variable=mid3 depth=1
    stream<datatype_16nt> ori_3[fusion_band];
#pragma HLS STREAM variable=ori_3 depth=1
    Dilatation(in, ori, mid1[0], ori_1[0]);
    for(unsigned char i=0; i<iteration-1; i++){
#pragma HLS UNROLL
        Erosion(mid1[i], ori_1[i], mid2[i], ori_2[i]);
        Max(mid2[i], ori_2[i], mid1[i+1], ori_1[i+1]);
    }
    Erosion(mid1[iteration-1], ori_1[iteration-1], mid3, ori_3);
    Max_last(mid3, ori_3, out);
}

```

Fig. 7. Sample code used for implementing morphological closing operation.

When the COPU starts to work, the new result pixel from RPU is firstly compared with a column of pixels ($r_a - 1$ pixels) being read from the BRAMs of size $(r_a - 1) \times N$. The width of the image is represented by N . Then, the pixels of the current column in BRAM are updated via reading the new pixel and removing the old one. The *array_reshape* directive we use can reduce the consumption of BRAMs while accessing data in parallel. It should be emphasized that a result pixel can be obtained by $2 \times (r_a - 1)$ comparisons for the optimized structure of dilatation/erosion, throughout which the computational efficiency is therefore improved.

With respect to its definition, the optimized dilatation reconstruction consists of k identical stages, where k is set to 20. The i stage uses the output of the $i-1$ stage as the marker input together with the buffered mask image, in which dilatation and point-wise minimum (Min in the Fig. 6) are performed at each stage. Similarly, erosion reconstruction is therefore designed with the same structure. Fig. 7 shows the sample code used to implement morphological closing operation in HLS. The *ori_1*, *ori_2*, *ori_3* in the figure is mainly used to cache the

mask image that is used for morphological closing operation. *HLS_UNROLL* directive is added to implement k instances of erosion reconstruction.

Finally, the subtraction is performed on the results of the opening and closing reconstruction, by which the final results are stored in the FIFO for the FCU.

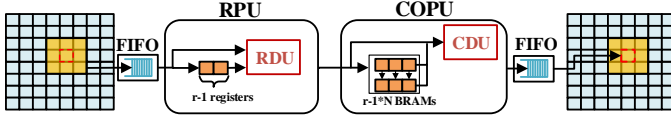


Fig. 8. The sharing architecture for dilation, erosion, and mean filter.

3) *FCU*: This section describes how to effectively implement the self-guided filter used for feature clustering in hardware, of whose pseudocode is shown in Algorithm 2. As the fundamental operation of the self-guided filter, the mean filter is a windowed operation with radius r_b , which undertakes the main computational burden. Particularly, the sharing architecture of erosion and dilation is also applicable for the mean filter, in which only the RED color highlighted RDU and CDU need to be redesigned. Specifically, the RDU and CDU in mean filter perform sum operation instead of comparison. Consequently, the mean value is computed by multiplying the window sum (column sum) by the value of $1/(2r_b + 1)^2$.

As shown in Algorithm 2, the simplified self-guided filtering in each PE only includes two mean filters which compute the values of $meanI$ and $CorrI$. The remaining values described in Algorithm 2 are computed using a set of arithmetic units, such as fixed-point multipliers, adders, subtractors, etc. We perform reasonable segmentation and sorting adjustments on complex coefficient operations, and propose a deep pipelined design to make the entire module with higher throughput. As shown in Fig. 9, the entire module is divided into 8 pipeline stages. Seven memory buffers of depth 1 are configured to resolve data dependencies amidst operations.

C. Highlight

Firstly, it is worth noting that different optimization strategies concerning arrays, loops, latency, and throughput are utilized to achieve a trade-off between area and processing speed through adding directives provided in HLS. For instance, the entire hardware implementation employs global deep pipelining, which takes advantage of the *dataflow* directive to raise the concurrency of the RTL implementation and overall throughput. The three units are connected by FIFO memories with a depth of 1. The C++ template class *hls::stream* <> is adopted for mapping these data to FIFOs.

Secondly, we design a sharing architecture for the core units of FCU and FLEU (erosion, dilation and mean filter) as shown in Fig. 8, which only requires reconfiguration of the RDU and CDU highlighted by RED. The general pipelined architecture based on row and column decomposition further simplifies the overall hardware implementation, thereby achieving high throughput performance.

TABLE I
DATA STRUCTURE FOR FAST-MGD IN FPGA IMPLEMENTATIONS

Fixed-pointed data	ap_ufixed<W,I>
$meanI$	ap_ufixed<18, 1>
$CorrI/VarI$	ap_ufixed<23, 1>
a/b	ap_ufixed<24, 1>
V/O	ap_ufixed<19, 1>

Thirdly, the type of the input data is 16-bit unsigned fixed-point (15 bits fractional part), and the type of intermediate data needs to be taken into account seriously due to its significant impact on detection accuracy and resource consumption. In particular, a large number of arithmetic units are designed in the FCU. As listed out in Table I, the appropriate data types provided by HLS are applied to different intermediate data by balancing detection accuracy and resource consumption. W and I in the table represent the word length in bits and the number of bits above the decimal point. ap_ufixed<> refers to the unsigned fixed-point.

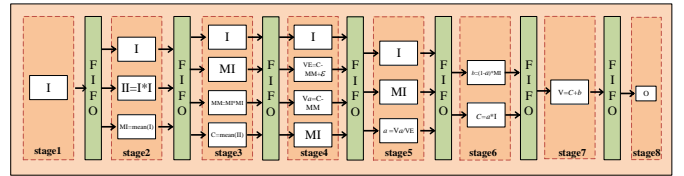


Fig. 9. Hardware structure of self-guided filter (the mean filter is represented by *mean*).

Fourthly, it needs to be emphasized that the proposed FPGA-based hardware architecture does not depend on any specific underlying physical devices of FPGA and vendor-provided IP cores. In addition, the framework supports HSIs with different bands and sizes. The parameters of each unit, the number of fused bands Q , the size of SE r_a , the radius of mean filter r_b , the iteration k and the smoothness of the guided filtering ϵ in HLS can be adjusted to adapt to various complicated scenarios. This parameter-configurable architecture improves scalability and portability of the whole structure.

VI. EXPERIMENTAL RESULTS AND ANALYSIS

This section is organized as follows. The hyperspectral datasets used for different scenes in the experiments are first introduced in Subsection VI-A. Subsection VI-B describes the parameter settings of the detection algorithms used for comparison and the proposed Fast-MGD. Subsection VI-C evaluates the detection accuracy of the software version using MATLAB of the proposed Fast-MGD by comparing the widely used detectors mentioned above. The effects of morphological reconstruction on feature localization and self-guided filter for feature clustering in Fast-MGD are further analyzed in Subsection VI-D. Subsection VI-E shows a comparison of the proposed hardware implementation evaluated on a Xilinx Virtex7 FPGA with the corresponding software version.

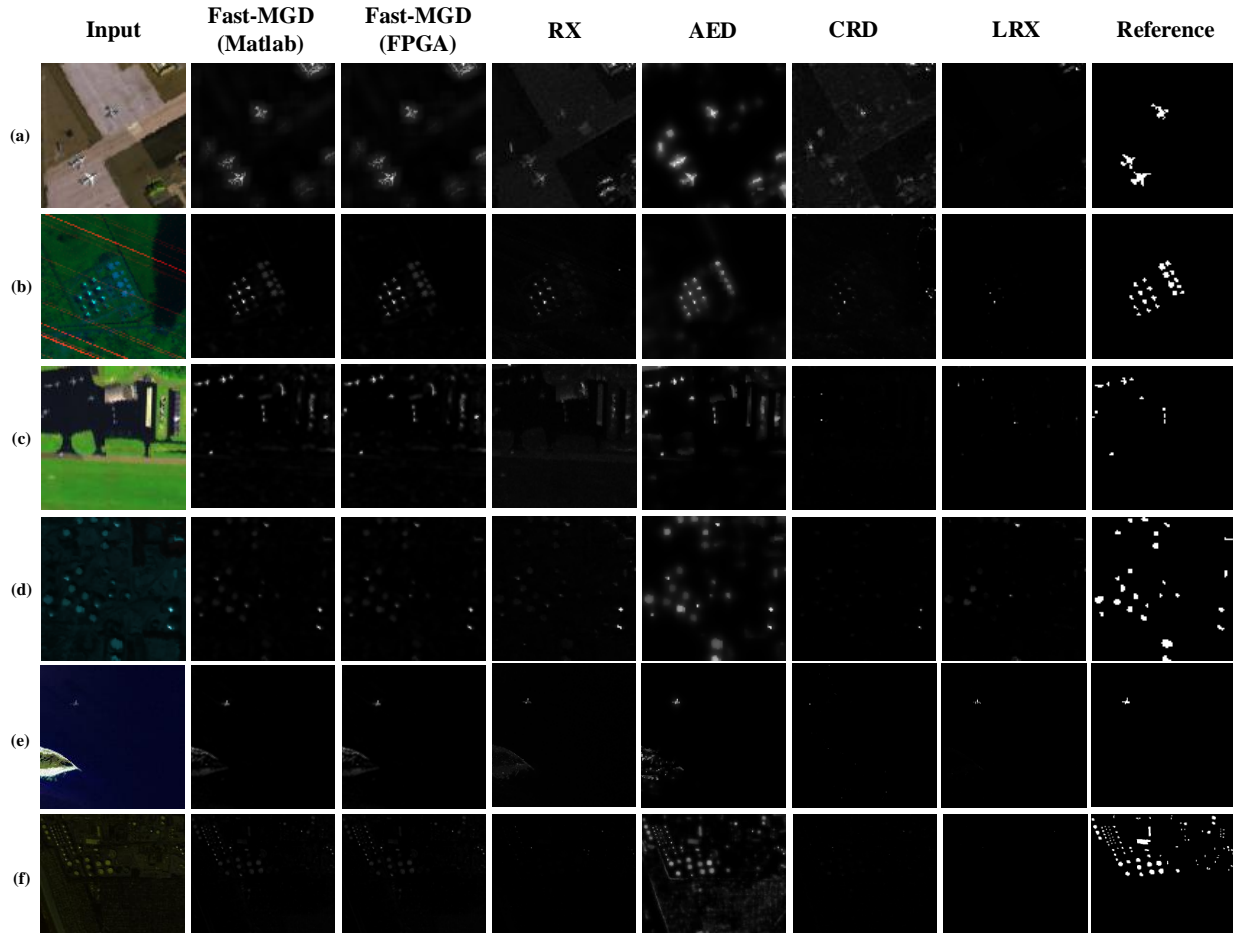


Fig. 10. Color composites of hyperspectral images and detection maps of the compared methods. The last column shows the reference detection maps, which are obtained by human labeling.

A. Hyperspectral Image Dataset

1) *San Diego Dataset*: The first dataset used in the experiments has been widely applied for target detection, which is captured by the Airborne Visible/Infrared Imaging Spectrometer (AVIRIS) sensor over the San Diego airport area, CA, USA. The original image contains 100×100 pixels with 224 bands, ranging from 400 to 2500 nm. Due to dense water vapor and atmospheric influence, 189 bands are retained after removing the noise bands (1-6, 33-35, 97, 107-113, 153-166, and 221-224). Three aircrafts are treated as the anomaly objects that need to be detected in the image. The sample image and the reference detection map is shown in Fig. 10 (a), respectively.

2) *Urban-Beach Dataset*: The Urban-Beach database collected by the AVIRIS Sensor was accessible on the website (<http://xudongkang.weebly.com>). The visualization images and corresponding maps are shown in Fig. 10 (b)-(e), respectively. These scenes consist of 100×100 (Fig. 10 (b)-(d)) or 150×150 (Fig. 10 (e)) pixels with different bands, in which the noise bands have been removed. The different spatial resolutions of these scenes are described in Table IV.

3) *EI Segundo Data Set*: The third dataset collected by the AVIRIS Sensor describes an area that is composed of oil refinery, several residential areas, parks and a school zone in

EI Segundo, CA, USA, which includes 224 spectral channels in the range of 366-2469 nm. The picture contains 2048 anomalous pixels with a spatial size of 250×300 and a ground resolution of 7.1 m. The visualization images and corresponding maps are shown in Fig. 10 (f), respectively.

B. Comparison of Methods and Parameter Settings

The detection accuracy of different algorithms can be evaluated by the receiver operating characteristic (ROC) through commonly used measurement [41]. Meanwhile, in order to further quantify the magnitude of the detectors' accuracy, the area under a ROC curve is adopted, being referred to as the area under the curve (AUC) [42]. As shown in Fig. 11, the true positive rate (TPR) on the vertical axis is defined as the proportion of correct positive results in all positive samples when a set of threshold (T) for the detection map is determined. Similarly, the false positive rate (FPR) on the horizontal axis indicates the proportion of false positive results in all negative samples. When the values of FPR are identical, the increase of the TPR value makes the AUC scores of (TPR, FPR) approaching the unity value, indicating that the detection accuracy is positively correlated with the performance of the algorithm. In this paper, the AUC score of (FPR, T) also

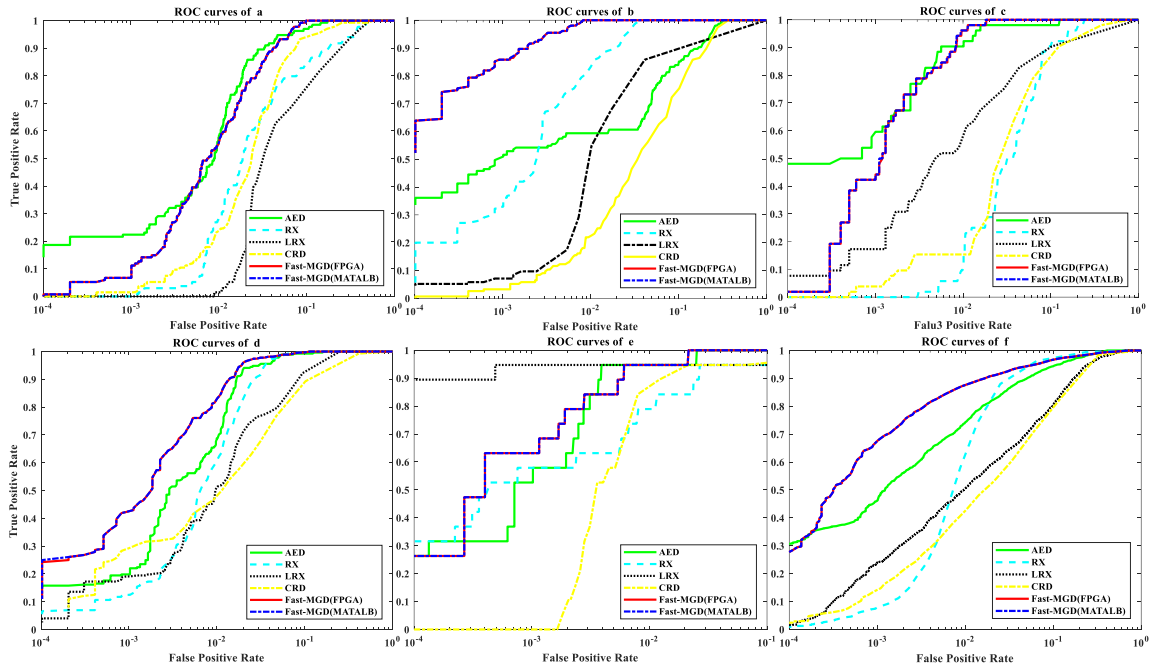
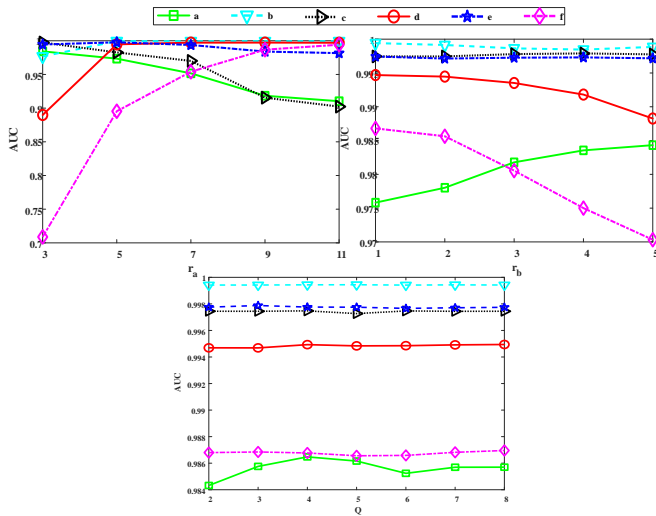


Fig. 11. ROC curves of the compared methods.


 Fig. 12. Parameter sensitivity analysis for parameter r_a , r_b , Q .

functions as a critical indicator to reflect the false alarm rate of the detection algorithm. Conversely, the AUC score of (FPR, T) approaching to zero implies lower false alarm rate, with which the detection accuracy is negatively correlated.

To verify the advantageous performance of Fast-MGD in hyperspectral anomaly detection, various detectors are involved concerning the AED [16], RX [7], LRX [10], and CRD [13] for comparison. In this case, we select the optimal parameters for the specific sample image in regard to the corresponding AUC scores of (FPR, TPR). For LRX algorithm, the proper size of the sliding window should be selected under scrutiny to achieve its optimal performance. For CRD algorithm, Li and Du confirmed that the regularization parameter λ being set to 10^6 can achieve the optimal detection accuracy. For AED

TABLE II
EVALUATION AUC SCORES OF (TPR, FPR) OBTAINED BY THE FAST-MGD(MATLAB), FAST-MGD(FPGA), RX, AED, CRD AND LRX

HSIs	Fast-MGD (Matlab)	Fast-MGD (FPGA)	RX	AED	CRD	LRX
a	0.98432	<u>0.98432</u>	0.94041	0.98499	0.96263	0.91430
b	<u>0.99942</u>	0.99942	0.99463	0.95585	0.93042	0.91571
c	<u>0.99745</u>	0.99745	0.95119	0.99554	0.93970	0.93420
d	<u>0.99470</u>	0.99470	0.98870	0.99131	0.95156	0.97054
e	<u>0.99776</u>	0.99776	0.98281	0.99740	0.96720	0.97495
f	0.98680	<u>0.98672</u>	0.98153	0.98038	0.93429	0.94439
Average	0.99341	<u>0.99340</u>	0.97321	0.98425	0.94763	0.94235

algorithm, the specific parameter settings involved in the AED algorithm have been described by its authors in detail.

For our proposed algorithm, the parameters Q , r_a , k and r_b used in the operations of feature location and feature clustering can be adjustably configured to adapt different scenes, thereby achieving optimal detection results. The AUC score of (TPR, FPR) is applied to evaluate the objective performance of the proposed algorithm by systematically varying the parameter settings one by one. Fig. 12 reports the effects of parameter settings on six real data sets, respectively. $Q = 2$ is adopted by finding a tradeoff between detection accuracy and resource consumption. While such a setting cannot achieve the best performance for individual sample images, it is designed for ensuring stable and acceptable performance for most of the tested HSIs. The size of SE r_a is set to 3 in Fig. 10 (a) and (c), to 5 in Fig. 10 (b), (d) and (e), and to 9 in Fig. 10 (f). A reasonably large value of r_a may enable the detection of certain false anomalous objects. Similarly, a relatively small r_a may result in failures to detect a large area of anomalous

information, imposing detrimental impacts on the performance of the detection accuracy. Both of the above cases attribute to the low detection accuracy and AUC scores. However, a default setting $k = 20$ is applicable for all sample images. The influence of k can be analyzed with more detailed descriptions in Section VI-D. $r_b = 1$ is set for most of the sample images. Exceptionally, r_b is adjusted to 5 for San Diego dataset to achieve better performance (Fig. 10 (a)).

C. Detection Results

For San Diego dataset, the AUC score of (TPR, FPR) is listed in Table II. The AUC score is 0.98432, which is much higher than RX (0.94094), CRD (0.96263), and LRX (0.91430). Compared with the AED method in which the AUC score of (TPR, FPR) is ranked the first, the detection map of the Fast-MGD method contains less background information in Fig. 10(a).

TABLE III
EVALUATION AUC SCORES OF (FPR, T) OBTAINED BY THE FAST-MGD(MATLAB), FAST-MGD(FPGA) AND AED

HSIs	Fast-MGD (Matlab)	Fast-MGD (FPGA)	AED
a	<u>0.0343</u>	0.0343	<u>0.0343</u>
b	<u>0.0059</u>	0.0059	0.0228
c	<u>0.0144</u>	0.0144	0.0218
d	<u>0.0062</u>	0.0062	0.0311
e	<u>0.0045</u>	0.0045	0.0051
f	<u>0.0063</u>	0.0063	0.0223
Average	<u>0.0119</u>	0.0119	0.0229

For Urban-Beach datasets, the proposed method in Table II shows the best AUC scores of (TPR, FPR) in all scenes. Compared with the RX, LRX, CRD, and AED, the detection results obtained by Fast-MGD achieve better visual inspection as shown in Fig. 10(b)-(e). Most of the anomalies in urban scenes (Fig. 10 (b)-(d)) are unable to be detected by the LRX and CRD methods, and the lousy AUC scores of (TPR, FPR) also support this observation. Nevertheless, these two methods display inadequate performance throughout with respect to computing time, not to mention the inconvenience induced in optimal settings of the inner and outer window. As shown in Fig. 11(e), LRX performs well on the ROC curve of this data set, yet it is worth noting that this method cannot have satisfying detection results on all HSIs. The RX method is merely suitable for sample images with simple background distribution and for large area anomalies, which is unsuitable for complex HSIs as shown in Fig. 10(c).

For EI Segundo dataset, the detection maps are shown in Fig. 10(f). By visualizing the final detection maps, we notice that the proposed Fast-MGD shows excellent performance on anomaly detection compared to other methods, whereby most of the anomalies in the sample image can be clearly observed. The AUC score of (TPR, FPR) shown in Table II further demonstrates the superiority our approach. Although the AUC score of (TPR, FPR) of RX is considerable, it does not perform well on visual effects. In addition, we also compare the ROC curves of different methods on the EI Segundo dataset. As

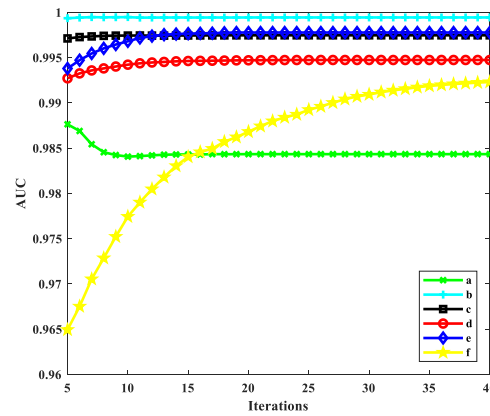


Fig. 13. Effect of the number of iterations on each dataset.

shown in Fig. 11(f), the proposed method exhibits higher true positive rates regardless the false positive rate fluctuating between 0 to 1.

The AED method comes the second out of the proposed methods in terms of the average AUC scores of (TPR, FPR). As shown in Fig. 11 (a), (c) and (d), the ROC curves of AED are close to that of the Fast-MGD in these three scenarios. In order to further compare the performance of AED with that of Fast-MGD, AUC scores of (FPR, T) are shown in Table III. It can be noticed that our algorithm has a lower false alarm rate while remaining higher detection accuracy. Further, we emphasize that the proposed Fast-MGD is a hardware-friendly algorithm featured with lower complexity.

D. Component Analysis

The opening/closing reconstruction performed in feature localization can effectively describe the spatial information of HSIs, which requires repeated combining of dilation/erosion and pointwise minimum/maximum operations until idempotency is reached. However, a great many of uncontrollable iterations sometimes may reach hundreds of times, of whose intricacies not only require a lot of time to tackle with, but also bring unbelievably high computational costs in hardware implementations.

As shown in Fig. 13, we analyze the impact of the number of iterations imposed on the AUC scores of (TPR, FPR) on each dataset while keeping other parameters remain optimal. It is surprising that the continuous increase in the number of iterations did not induce correlated rise in the curve of AUC, instead, it continued to rise first and then tended to stabilize. For example, the AUC score obtained through the proposed Fast-MGD is 0.99776 for e dataset (Fig. 10 (e)) when the number of iterations increases to 20. Subsequently, the continuous increase in the iterations does not bring any evident improvements in the AUC scores, upon which the number of iterations can be set to a fixed value for all images without considering idempotency. This operation can significantly reduce the computational costs and memory consumption at the expense of a merely small loss in detection accuracy. However, we can observe that the AUC score of a dataset (Fig. 10 (a)) is the highest (0.98762) when the iterations is 5. It is worth noting that too small iterations will lead to a high false alarm

TABLE IV
SUMMARY OF RESOURCE UTILIZATION FOR THE FPGA-BASED IMPLEMENTATION IN HSIs WITH DIFFERENT SPATIAL RESOLUTIONS AND SPECTRAL BANDS

HSIs	Size of image	Resolutions	LUTs (433200)	LUTRAMs (174200)	FFs (866400)	BRAMs (1470)	DSPs (3600)
a	100×100×189	3.5m	33969(7.84%)	836(0.48%)	49964(5.77%)	108(7.35%)	12(0.33%)
b	100×100×207	17.2m	43890(10.13%)	1001(0.57%)	57962(6.69%)	141(9.59%)	12(0.33%)
c	100×100×191	3.5m	31681(7.31%)	287(0.16%)	46370(5.35%)	99(6.73%)	12(0.33%)
d	100×100×205	7.1m	43874(10.13%)	1001(0.57%)	57929(6.69%)	141(9.59%)	12(0.33%)
e	150×150×188	17.2m	44906(10.37%)	1003(0.58%)	58593(6.76%)	141(9.59%)	12(0.33%)
f	250×300×224	7.1m	67591(15.60%)	963(0.55%)	83713(9.66%)	262.5(17.86%)	12(0.33%)

rate. For example, when $k = 5$, AUC score of (FPR, T) is as high as 0.0410 for a dataset. A large number of experiments have found that a default setting $k = 20$ can enable most tested HSIs to perform well in false alarm rate and detection accuracy.

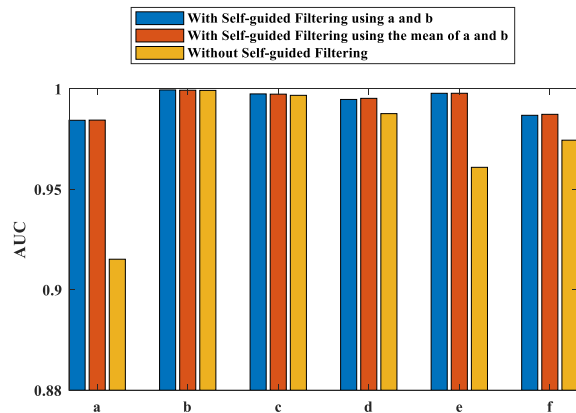


Fig. 14. AUC scores of (FPR, TPR) on Fast-MGD with simplified self-guided filtering, with self-guided filtering and without the self-guided filtering.

We also analyze the influence of the simplified self-guided filtering over additional experiments on each dataset. As shown in Fig. 14, we calculated the AUC scores of (TPR, FPR) on Fast-MGD with simplified self-guided filtering, with self-guided filtering and without self-guided filtering respectively. The third column clearly shows that self-guided filtering significantly improves the detection accuracy. Moreover, the beneficial advantages of using the spatial correlation amidst adjacent pixels of HSIs for anomaly detection are therefore validated. By observing the first and second columns of the histogram, it can be concluded that the negative impact of the mean of a and b on detection performance can be almost ignored compared with the reduction of memory consumption.

E. FPGA Implementation

1) *Comparison of Detection Accuracy Performance:* It should be pointed out that the parameter settings in the FPGA implementation of Fast-MGD are identical with that in the software. As shown in Fig. 10, the proposed FPGA implementation shares almost the same visual effects with the software version. In Fig. 11, the ROC curves of hardware

implementation are depicted. It is noticeable that the ROC curves of all scenes are very close to that of the software, which is attributed to the reasonably sound setting of intermediate data types in the FCU. Hence, the proposed deep pipelined hardware architecture is proven to be far more than satisfactory.

2) *Performance Evaluation:* A Virtex7 XC7VX690T FPGA used to evaluate the proposed Fast-MGD hardware architecture contains 433,200 LUTs, 174,200 LUTRAMs, 866,400 FFs, 1470 Block RAMs, and 3600 DSPs. Table IV shows the resource utilization corresponding to Fast-MGD for diverse images with different sizes and resolutions using Vivado 2018.3. Optimal configurable parameters are set for different HSIs to ensure excellent detection accuracy.

As illustrated in Table IV, a constant number of DSPs are mainly used to implement the multiplication of Stage 2, Stage 3, and Stage 6 in FCU. Most FIFOs of depth 1 are inferred as LUTs and FFs. The consumption of BRAMs is mainly due to COPU in the sharing architecture. The number of LUTs, LUTRAMs, FFs and BRAMs is related to the image size and the setting of configurable parameters. Also, Table V lists out the resource summary of dilation using Vivado HLS for fixed image size (100×100) and varied r_a to further quantitatively evaluate the designed sharing architecture. The estimation of the fastest achievable clock frequency is 3.310 ns. The number of LUTs, FFs, and BRAMs increase linearly with r_a . In summary, our cost-effective hardware designing philosophy using HLS aims at supporting HSIs with different sizes and spectral bands through adjustment of several configurable parameters including Q , k , r_a and r_b .

Three different platforms including C++ using OpenMP, MATLAB and FPGA for the above six datasets are adopted in our experiments. The code of software version using MATLAB R2017b and Visual studio 2017 is executed on Window 10 operation system equipped with the Intel Core (TM) quad CPU @2.50 GHz and 8 GB main memory. The hardware architecture is implemented by using Vivado HLS 2018.3. The clock frequency of the Virtex7 FPGA is set at 200 MHz. As shown in Table VI, the processing time of FPGA-based implementation achieved a speedup of more than 161 times higher than that of the MATLAB and more than 72 times higher than that of C++.

It must be emphasized that the processing time achieved

TABLE V
THE MEASURED PERFORMANCE OF DILATION USING 100×100 IMAGES

r_a	3	5	7	9	11
Latency	10808	11629	11640	12065	12496
BRAM_18Ks	1	2	3	4	5
FFs	298	469	765	1158	1601
LUTs	496	738	1014	1354	1726

TABLE VI
PROCESSING TIME MEASURED FOR FAST-MGD METHODS IN MATLAB, C++, AND FPGA IMPLEMENTATIONS

HSIs	MATLAB (ms)	C++(ms)	FPGA (ms)
a	224	45	0.528
b	232	55	0.553
c	212	45	0.513
d	232	54	0.553
e	408	122	1.176
f	1265	573	7.856

by the FPGA implementation of Fast-MGD is strictly in real-time for all the above scenarios. In specific, the scanning time of cross-track line in AVIRIS being referred to as a push-broom instrument, is quite fast (8.3ms to collect 512 full-pixel vectors) [43], indicating that the processing time of the scene must be limited within 0.16s (in Fig. 10(e) and (f) the values are the exceptional 0.36 and 1.216) to achieve real-time performance thoroughly. From Table VI, it can be observed that the processing times are lower than 8 ms, including the load time and the data transfer time from the CPU to FPGA device.

In summary, the proposed hardware implementation characterized by low resource utilization and real-time processing speed displays its great advantages in on-board anomaly detection.

VII. CONCLUSION

Most of the existing hyperspectral anomaly detection methods have so far met with limited success due to high computational complexity, to which their inability in on-board implementation is therefore proven. In this paper, a low-complexity anomaly detection algorithm and its corresponding cost-effective hardware architecture on FPGA are proposed. Experimental results on several real HSIs further verified the superiority of the Fast-MGD and its implementation with respect to computational complexity, processing speed, detection accuracy as well as resource consumption, thereby validating its suitability for on-board application.

Despite the above advantages of our method, certain shortcomings should be pointed out concerning related parameter settings which may impose encumbrance on its application. Our future research interests will focus on the adaptive parameter-determination techniques, paving way for the improvement in the generality and robustness of our model.

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